

No. STX-107



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# THE UNIVERSAL IMPEDANCE BRIDGE

1. Why can't we simply measure L and C instead of  $L_s$  or  $L_p$  and  $C_s$  or  $C_p$ ?

2. Why does a universal impedance bridge measure D and Q instead of the equivalent series resistance or parallel conductance?

3. When is the null good enough to take advantage of the accuracy built into the bridge?

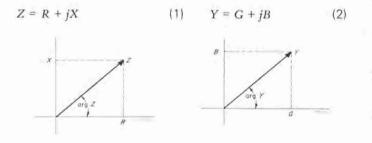
4. When does the nulling procedure converge rapidly? Is there a cure for an extreme case of "sliding null"?

5. Is there a better balancing procedure than flailing about with the controls?

6. Bridges require a certain amount of skill and knowledge of the operator. Why, then, does one use a bridge instead of one of the direct-reading instruments such as ohmmeters and inductance meters?

### IMPEDANCES AND ADMITTANCES AND THEIR EQUIVALENT CIRCUITS

A two-terminal device can be characterized by either its impedance or its admittance.



 $\arg Z = \tan^{-1} \frac{X}{R}$  (3)  $\arg Y = \tan^{-1} \frac{B}{G}$  (4)

Since the device's impedance and admittance are reciprocals of each other, we have

$$R + jX = \frac{1}{G + jB} = \frac{G - jB}{G^2 + B^2}$$
(5)

and

$$G + jB = \frac{1}{R + jX} = \frac{R - jX}{R^2 + X^2}.$$
 (6)

Simply by inspecting these equations and equating real and imaginary parts, we can write down all the relations between the Z- and Y- components:

$$R = \frac{G}{G^2 + B^2}$$
 (7)  $G = \frac{R}{R^2 + X^2}$  (8)

$$X = \frac{-B}{G^2 + B^2}$$
(9)  $B = \frac{-X}{R^2 + X^2}$ (10)

Notice also that

$$\arg Z = \arg \frac{1}{Y} = -\arg Y. \tag{11}$$

The characterization of a device in terms of its impedance suggests that we can represent it by an equivalent circuit consisting of a resistance and reactance in series, while the characterization of the same device by its admittance implies an equivalent circuit that consists of a conductance and susceptance in parallel,



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Any device can be represented (at a particular frequency) by either the series or parallel equivalent circuit.

**Problem:** When the rms voltage across the terminals of a particular device is 10 volts, the rms current is 5 amperes. The current leads the voltage by 30 degrees. What are the resistive and reactive components R and X of the device's impedance? What are the conductive and susceptive components G and B of the admittance? Draw the series equivalent circuit. Is the reactance an inductor or a capacitor? Draw the parallel equivalent circuit. Is the susceptance an inductor or a capacitor?

It is important to bear in mind that while any two-terminal physical device can be represented at a given frequency by either the series or parallel equivalent circuit, the equivalent circuit will in general not have the same impedance or admittance as the device ar any other frequency. This is because, to take the series equivalent for example, we must expect the impedance of a real device to have a resistive component that varies with frequency, and a reactive component whose magnitude is not proportional to  $\omega$  or  $1/\omega$ . Obviously we cannot represent such a device at all frequencies by a series resistor and inductor or capacitor whose values do not change with frequency. The most one can hope for is that one of the equivalent circuits, but not both, may have element values that are fairly constant over part of the frequency range of interest.

When we talk about inductors and capacitors, it is customary to describe the amount of loss in terms not of R or Gbut of the **quality factor** Q or **dissipation factor** D. The quantities Q and D convey exactly the same information since they are just reciprocals of each other.

$$Q = \frac{1}{D} = \tan \left| \arg Z \right| = \frac{|X|}{R} = \tan \left| \arg Y \right| = \frac{|B|}{G} \quad . \tag{12}$$

The absolute magnitude signs around the angles in (12) mean that Q and D are always positive numbers regardless of whether the device in question is inductive or capacitive. We usually use Q to describe inductors and D to describe capacitors. A low-loss component is one whose impedance (admittance) is almost purely reactive (susceptive) and hence has a high Q and low D.

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#### CAPACITORS

A device is said to be capacitive when the current through it leads the voltage across it in phase. Thus the impedance of a capacitive device has a negative reactive part and the admittance has a positive susceptive part, and the equivalent circuits are a series resistance  $R_S$  and capacitance  $C_S$  or a parallel conductance  $G_P$  and capacitance  $C_P$ .



Referring to the definition (12), we see that the dissipation factor D of a capacitive device is given by

(13)

(14)

$$D = \frac{R_{\rm S}}{\frac{1}{\omega C_{\rm S}}} = \frac{G_{\rm P}}{\omega C_{\rm P}} \quad . \tag{1.5}$$

Notice that, although D is the same whether we are talking about the series or parallel equivalent circuits, the two formulas for D are different.

If we substitute (15) into (13) and (14) in order to replace  $R_S$  and  $G_P$  by D, we obtain the expressions

$$Z = \frac{1}{\omega C_{\rm S}} (D-j) \qquad (16) \qquad Y = \omega C_{\rm P} (D+j) . \tag{17}$$

Sometimes one wishes to calculate the element values of one equivalent circuit from those of the other. From (16) and (17) we have

$$\frac{1}{\omega C_{\mathsf{S}}} \left( D - j \right) = \frac{1}{\omega C_{\mathsf{P}}} \left( D + j \right) = \frac{1}{\omega C_{\mathsf{P}}} \cdot \frac{D - j}{D^2 + 1}$$

whence

 $Z = R_S - j$ 

$$C_{\rm S} = C_{\rm P} \left( D^2 + 1 \right)$$
 (18)  $C_{\rm P} = C_{\rm S} \frac{1}{D^2 + 1}$  (19)

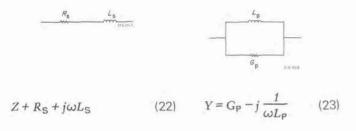
$$R_{\rm S} = \frac{D}{\omega C_{\rm S}} = \frac{1}{\omega C_{\rm P}} \cdot \frac{D}{D^2 + 1} \qquad G_{\rm P} = D\omega C_{\rm P} = \omega C_{\rm S} \frac{1}{D^2 + 1} .$$
(20) (21)

Question: If  $C_S$  and  $R_S$  are constant as the frequency varies, will  $C_P$  and  $G_P$  also be constant?

**Question:** Usually we talk about the plain "capacitance" of a capacitor, not  $C_S$  or  $C_P$ . Strictly speaking, does such a quantity exist? How much difference is there between  $C_S$  and  $C_P$  if the capacitor is fairly lossless ( $D \approx 0.001$ , say)?

### INDUCTORS

The series and parallel equivalent circuits for inductive devices are



The Q of an inductive device is given by

$$Q = \frac{\omega L_{\rm S}}{R_{\rm S}} = \frac{1}{G_{\rm P}\omega L_{\rm P}} \tag{24}$$

and in terms of Q,

$$Z = \omega L_{\rm S} \left(\frac{1}{Q} + j\right) \qquad (25) \qquad Y = \frac{1}{\omega L_{\rm P}} \left(\frac{1}{Q} - j\right), \quad (26)$$

We can easily obtain the relations between the two equivalent circuits just as we did above in the capacitive case.

$$L_{\rm S} = L_{\rm P} \frac{1}{\frac{1}{Q^2} + 1}$$
(27)  $L_{\rm P} = L_{\rm S} \left(\frac{1}{Q^2} + 1\right)$ (28)

$$R_{\rm S} = \omega L_{\rm P} \frac{Q}{Q^2 + 1}$$
 (29)  $G_{\rm P} = \frac{1}{\omega L_{\rm S}} \cdot \frac{Q}{Q^2 + 1}$  (30)

**Question:** When we talk about a "10-mH choke," do we mean that  $L_{\rm S}$  = 10 mH or that  $L_{\rm P}$  = 10 mH?

**Exercise:** A particular ferrite bead choke has an impedance at 100 MHz of 115 ohms. If the resistive component of this impedance is 100 ohms, what is Q? What is  $L_S$ ? What are  $L_P$  and  $R_P$ ?

**Question:** Ferrite bead chokes look like short circuits at dc. Why is the series ac resistance  $R_S$  at 100 MHz so much higher than the dc resistance?

Exercise: Show that

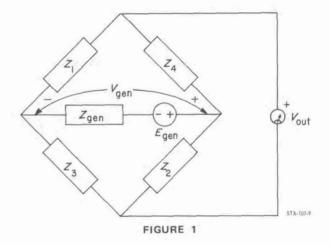
$$Q = \frac{1}{2\omega} \cdot \frac{average energy stored in L_S \text{ or } L_P}{average power dissipated in R_S \text{ or } G_P}$$

Why does this formula differ by a factor of 2 from the similar formula that gives the Q of a resonant circuit?

### THE BRIDGE BALANCE CONDITION

The general circuit of a bridge is shown in Figure 1. If we assume that the meter impedance is infinite we may regard  $Z_1$  and  $Z_4$  as one voltage divider and  $Z_3$  and  $Z_2$  as another. It is then apparent that

$$V_{\text{out}} = \frac{Z_1}{Z_1 + Z_4} - \frac{Z_3}{Z_2 + Z_3} \cdot V_{\text{gen}}$$
$$= \frac{Z_1 Z_2 - Z_3 Z_4}{(Z_1 + Z_4) (Z_2 + Z_3)} \cdot V_{\text{gen}} . \tag{31}$$



The bridge will "balance" - the output voltage will be zero - when the numerator of (31) is zero.

balance condition: 
$$Z_1Z_2 - Z_3Z_4 = 0$$
 (32)

Since no current flows in the meter branch at balance, our assumption that the meter impedance is infinite is not necessary for (32) to be valid. The balance condition does not depend on either the generator or the meter.

The complex balance condition (32) yields two real equations. Equating magnitudes and angles, we have

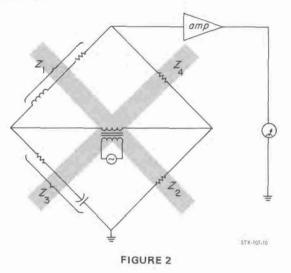
$$|Z_1| |Z_2| = |Z_3| |Z_4| \tag{33}$$

and

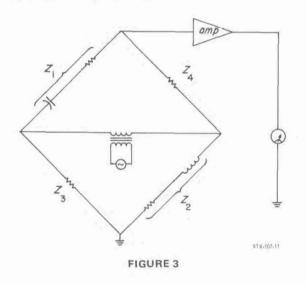
$$\arg Z_1 + \arg Z_2 = \arg Z_3 + \arg Z_4 \quad . \tag{34}$$

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Equations 33 and 34 must both be satisfied when the bridge is balanced.

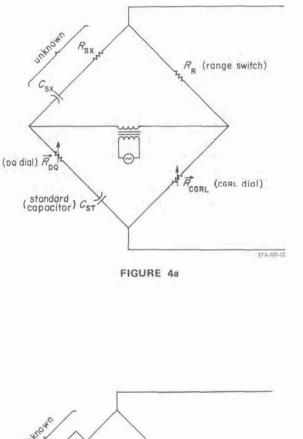


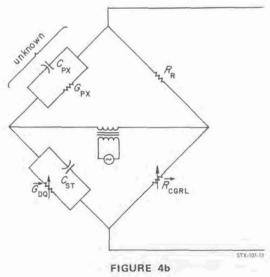
Equation 34 tells us what combinations of bridge arms have a chance of balancing. For example, the arrangement of Figure 2 can't possibly balance because  $\arg Z_1$  is positive,  $\arg Z_3$  is negative, and  $\arg Z_2$  and  $\arg Z_4$  are zero, so that  $\arg Z_1 + \arg Z_2$  could never equal  $\arg Z_3 + \arg Z_4$ . The bridge of Figure 3, on the other hand, could balance if the element values were properly adjusted.



THE SIX BRIDGE CONFIGURATIONS

Accurate variable inductors and capacitors are very costly, so we don't want to use them in a bridge if we can help it. Inductors are never very ideal; they have distributed capacitance, they have low Q at low frequencies, and the resistive part of either of their equivalent circuits is likely to be frequency dependent. Therefore we don't want to use fixed inductors either. With these constraints on the choice of parts to use in the arms, all universal bridges turn out about the same: two variable resistors, a fixed capacitor, and several fixed resistors. In the General Radio Type 1650 Bridge, these basic components are switched, depending on the nature of the unknown immittance that is to be measured, into one of the configurations shown in Figure 4. Capacitive unknowns whose dissipation factors are not too large are regarded as series-equivalent circuits and the bridge of Figure 4a measures  $C_{SX}$  and  $D_X$ . Capacitive unknowns with large loss are treated as parallel-equivalent circuits and the bridge of Figure 4b measures  $C_{PX}$  and  $D_X$ . Conversely, low-loss inductive unknowns are treated as parallel-equivalent circuits and lossy inductances as series-equivalent circuits. The bridges of Figures 4c and 4d measure respectively  $L_{SX}$ ,  $Q_X$  and  $L_{PX}$ ,  $Q_X$ . Resistance and conductance are measured by the bridges of Figures 4e and 4f.





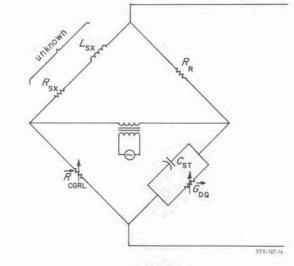
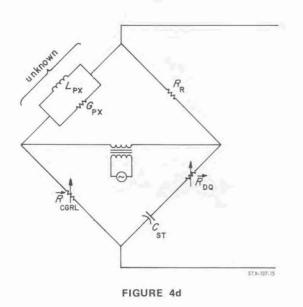
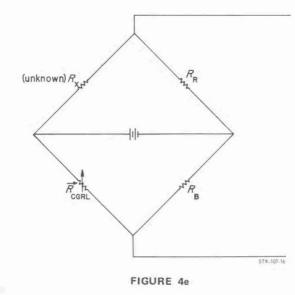
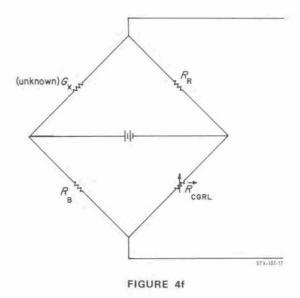


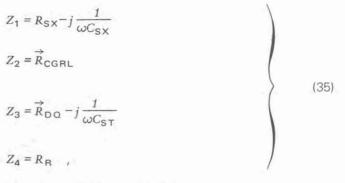
FIGURE 4c







Let us look at the balance condition of the series capacitance bridge of Figure 4a. Comparison with Figure 1 shows that



so that the balance equation 32 becomes

$$\left(R_{SX} - j\frac{1}{\omega C_{SX}}\right) \vec{R}_{CGRL} \text{ (bal)} = \left(\vec{R}_{DQ} \text{ (bal)} - j\frac{1}{\omega C_{ST}}\right) R_{R},$$
(36)

whence, equating real and imaginary parts, we get

$$C_{\mathsf{SX}} = \frac{C_{\mathsf{ST}}}{R_{\mathsf{R}}} \overrightarrow{R}_{\mathsf{CGRL}} \quad \text{(bal)}$$
(37)

and

$$R_{SX} = \frac{R_{R}}{\overrightarrow{R}_{CGRL} \text{ (bal)}} \overrightarrow{R}_{DQ} \text{ (bal)}, \qquad (38)$$

Equation 37 shows that when the bridge is balanced,  $C_{\rm SX}$  is determined from the balance setting of only one of the bridge variables,  $\vec{R}_{\rm CGRL}$ ; hence the *CGRL* dial can be calibrated so as to read  $C_{\rm SX}$  directly. But  $R_{\rm SX}$  depends upon the balance settings of both bridge variables,  $\vec{R}_{\rm CGRL}$  and  $\vec{R}_{\rm DQ}$ ; so we cannot read  $R_{\rm SX}$  directly from a single dial on the bridge. However if we multiply equation 38 by  $\omega C_{\rm SX}$ ,



FIGURE 5 The General Radio Type 1650-B Impedance Bridge,

ure 4b, we see that

and at the same time get rid of  $\vec{R}_{CGRL}$  (bal) by substituting from (37), we have

$$R_{SX}\omega C_{SX} = D_X = \omega C_{ST} \vec{R}_{DQ}$$
 (bal) , (39)

which shows that at balance the *D* of the unknown is equal to the *D* of the  $C_{ST}\vec{R}_{DQ}$  branch. Since  $D_X$  is proportional to the balance setting of  $\vec{R}_{DQ}$  and also to the frequency, the *DQ* dial can be provided with a scale that is proportional to  $D_X$  divided by the frequency. On the Type 1650 Bridge the reading on the series capacitance *D*-scale is equal to  $D_X/f(kHz)$ .

Equation 39 also helps explain why the series capacitance configuration is not used when  $D_X$  is large. Large values of  $\vec{R}_{DQ}$  would be needed to balance unknowns with large *D*'s. But if  $\vec{R}_{DQ}$  is made too large, the unavoidable stray capacitance in the *DQ* rheostat would introduce a non-negligible shunt capacitance branch into  $Z_3$ , equation 36 would have to be modified, and equation 37 would no longer be valid. Thus  $\vec{R}_{DQ}$  must be limited to reasonable values for the sake of accuracy in the *CGRL*-dial reading.

$$Z_{1} = \frac{1}{G_{PX} + j\omega C_{PX}}$$

$$Z_{2} = \vec{R}_{CGRL}$$

$$Z_{3} = \frac{1}{\vec{G}_{DQ} + j\omega C_{ST}}$$

$$Z_{4} = R_{R}$$
(40)

Turning now to the parallel-capacitance bridge of Fig-

The balance equation, which is now

$$\frac{1}{G_{PX} + j\omega C_{PX}} \cdot \vec{R}_{CGRL} \text{(bal)} = \frac{1}{\vec{G}_{DQ} \text{(bal)} + j\omega C_{ST}} \cdot R_{R},$$
(41)

gives us the two relations

$$C_{\mathsf{PX}} = \frac{C_{\mathsf{ST}}}{R_{\mathsf{R}}} \quad \vec{R}_{\mathsf{CGRL}} \text{ (bal)}$$
(42)

and

$$G_{\mathsf{PX}} = \frac{\vec{G}_{\mathsf{DQ}} \text{ (bal)} \vec{R}_{\mathsf{CGRL}} \text{ (bal)}}{R_{\mathsf{R}}}$$
(43)

or

$$D_{\rm X} = \frac{1}{\vec{R}_{\rm DQ}(\text{bal}) \ \omega C_{\rm ST}} \quad . \tag{44}$$

Equation 42 for  $C_{PX}$  is identical with equation 37 for  $C_{SX}$ , so a single scale on the *CGRL* dial reads both  $C_{SX}$  when the bridge is in the series capacitance configuration and  $C_{PX}$  when it is in the parallel capacitance configuration. But equation 44 is not the same as equation 39. This time  $D_X$  is inversely proportional to the balance setting of  $\vec{R}_{DQ}$  and to the frequency. This is why the parallel-capacitance *D*-scale reading on the 1650 is equal to  $D \times f(\text{kHz})$ , and why its readings increase in the opposite direction from those on the series-capacitance *D*-scale. Equation 44 also shows that the parallel-capacitance bridge is used for unknowns with large values of *D* for exactly the same reason that the series capacitance bridge is used for low-*D* unknowns: to avoid large values of  $\vec{R}_{DQ}$ .

Calculations similar to the ones we have just gone through lead to the following balance relations for the seriesand parallel-inductance bridges of Figures 4c and 4d, the resistance bridge of Figure 4e, and the conductance bridge of Figure 4f.

$$L_{SX} = R_{R}C_{ST}\vec{R}_{CGRL} \text{ (bal)}$$
 series (45)  
$$Q_{X} = \omega C_{ST}\vec{R}_{DQ} \text{ (bal)}$$
 (46)

$$L_{\mathsf{PX}} = R_{\mathsf{R}} C_{\mathsf{ST}} \vec{R}_{\mathsf{CGRL}}(\mathsf{bal})$$
 (47)

$$Q_{\rm X} = \frac{1}{\omega C_{\rm ST} \vec{R}_{\rm DQ}(\rm bal)} \tag{48}$$

$$R_{\rm X} = \frac{R_{\rm B}}{R_{\rm B}} \vec{R}_{\rm CGRL}$$
 (bal) resistance (49)

$$G_{\rm X} = \frac{1}{R_{\rm B}R_{\rm R}} \vec{R}_{\rm CGRL} \text{ (bal)}$$
 conductance (50)

balanced. Taking the series-capacitance bridge as our example, we shall calculate the sensitivity of the null to small adjustments of the *CGRL* and *DQ* dials.

We suppose that the bridge is very near balance. Let  $\vec{R}_{CGRL}$  and  $\vec{R}_{DQ}$  differ from the balance settings  $\vec{R}_{CGRL}$  (bal) and  $\vec{R}_{DQ}$  (bal) by small amounts  $\Delta \vec{R}_{CGRL}$  and  $\Delta \vec{R}_{DQ}$ . If we make the substitution

$$\vec{R}_{CGRL} = \vec{R}_{CGRL}(bal) + \Delta \vec{R}_{CGRL}$$
(51)

and

$$\vec{R}_{DQ} = \vec{R}_{DQ} (\text{bal}) + \Delta \vec{R}_{DQ}$$
(52)

in the bridge arm impedances (35) and then put these impedances into (31), we shall obtain an expression for  $V_{out}$  as a function of  $\Delta \vec{R}_{CGRL}$  and  $\Delta \vec{R}_{DQ}$ .

We can save ourselves considerable work if we exploit the fact that near balance the numerator of equation 31 is almost zero. The differential of a fraction is given by

$$d\left(\frac{N}{D}\right) = \frac{DdN - NdD}{D^2}$$

and if N is very small we have approximately

$$d\left(\frac{\underline{N}}{\underline{D}}\right) = \frac{dN}{\underline{D}}$$

Thus we may ignore the effect of  $\Delta \vec{R}_{CGRL}$  and  $\Delta \vec{R}_{DQ}$  on the denominator of (31). When we substitute (35), (51), and (52) into (31), leaving  $\Delta \vec{R}_{CGRL}$  and  $\Delta \vec{R}_{DQ}$  out of the denominator and noting that most of the terms in the numerator cancel because of the balance condition (36), we have

$$V_{\text{out}} = \frac{\left(R_{\text{SX}} - j\frac{1}{\omega C_{\text{SX}}}\right) \Delta \vec{R}_{\text{CGRL}} - R_{\text{R}} \Delta \vec{R}_{\text{DQ}}}{\left(R_{\text{SX}} - j\frac{1}{\omega C_{\text{SX}}} + R_{\text{R}}\right) \left(\vec{R}_{\text{CGRL}} + \vec{R}_{\text{DQ}} - j\frac{1}{\omega C_{\text{ST}}}\right)} V_{\text{gen}}$$
(53)

The bridge detector does not measure  $V_{out}$ ; it measures  $|V_{out}|$ . If we multiply equation 53 by its complex conjugate, we can obtain, after considerable arranging of terms and some substitutions from equations 37, 38, and 39,

$$|V_{out}|^{2} = \frac{\omega^{2} R_{B}^{2} C_{SX}^{2}}{[(D_{X} + \omega R_{B} C_{SX})^{2} + 1]^{2}} \times \left\{ \delta_{CGRL}^{2} + D_{X}^{2} (\delta_{CGRL} - \delta_{DQ})^{2} \right\} |V_{gen}|^{2} ,$$
(54)

#### NULLS

Thus far in our discussion we have assumed that the bridge is already balanced; now let us look at the way it gets

where  $\delta_{CGRL} = \Delta \vec{R}_{CGRL} / \vec{R}_{CGRL}$  (bal) and  $\delta_{DQ} = \Delta \vec{R}_{DQ} / \vec{R}_{DQ}$  (bal). Equation 54 expresses the bridge output voltage as a function of the fractional deviations  $\delta_{CGRL}$  and  $\delta_{DQ}$  of the *CGRL* and *DQ* dials from their balance settings.

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Although equation 54 was derived for the series capacitance bridge, essentially the same result is found for the parallel capacitance and series and parallel inductance bridges.

Let us look particularly at the quantity within  $\{\}$ 's in equation 54, for this factor places in evidence the most important characteristics of the null. The occurrence of the product  $\delta_{CGRL}\delta_{DQ}$  shows that the nulling process is not orthogonal, that is to say, a setting of either the *CGRL* or the *DQ* dial that gives a minimum bridge output is upset by subsequent adjustment of the other dial. (This phenomenon is called a "sliding null.") Thus, even in principle, a null has to be arrived at through a series of alternate adjustments to the *CGRL* and *DQ* dials for minimum output, a process that converges the more slowly the larger the dissipation factor  $D_X$ .

When we adjust  $\vec{R}_{\text{CGRL}},$  we find a minimum of  $|\mathcal{V}_{\text{out}}|^2$  when

$$\delta_{\text{CGRL}} = \frac{D_{\text{X}}^2}{1 + D_{\text{X}}^2} \delta_{\text{DQ}} , \qquad (55)$$

and when we adjust  $\vec{R}_{DQ}$ , this minimum occurs when

$$\delta_{DQ} = \delta_{CGRL} \,, \tag{56}$$

If  $D_X$  is very small, the  $\delta^2_{CGRL}$  term dominates (54), and the bridge is relatively insensitive to the DQ dial. In this case convergence of the nulling procedure is rapid, since each adjustment of the CGRL dial makes  $|\delta_{CGRL}|$  much smaller than  $|\delta_{DQ}|$ , and the succeeding adjustment of the DQ dial makes  $\delta_{DQ} = \delta_{CGRL}$ .

As  $D_X$  becomes larger, the influence of the term  $D_X^2$  $(\delta_{CGRL} - \delta_{DQ})^2$  increases and convergence becomes slower, We can see in (55) that each adjustment of the *CGRL* dial reduces  $|\delta_{CGRL}|$  less and less as  $D_X$  increases. When  $D_X$  is more than about 5 the null can no longer be found at all because the bridge's resolution does not permit us to see a new minimum as we move the *CGRL* dial away from its previous setting,

The Type 1650 achieves the effect of an orthogonal null for large *D* (small *Q*) values by means of a nonreciprocal mechanical coupling (Orthonull<sup>®</sup>) between the *CGRL* and *DQ* dials. When Orthonull is switched in, a movement of the *CGRL* dial also moves the *DQ* dial so as to keep the ratio  $\vec{R}_{CGRL}/\vec{R}_{DQ}$  constant, but a movement of the *DQ* dial does not affect the *CGRL* dial. Thus when the *CGRL* dial is being adjusted, the term  $D_X^2 (\delta_{CGRL} - \delta_{DQ})^2$  in equation 54 is constant and the bridge output is sensitive to the term  $\delta_{CGRL}^2$ , just as though  $D_X$  were very small.

### ACCURACY

High loss, high frequency, and either very large or very small unknown impedances decrease the accuracy of the bridge.

One can easily understand why accuracy is adversely affected by loss. Information about the unknown *L* or *C* is contained in the imaginary part of the unknown immittance, and when the unknown device is very lossy this imaginary part is swamped by a much larger real part. The decrease in accuracy is thus due to the bridge's limited resolution. When  $D_X$  is large (say 5 or 10), the output voltage is very much more sensitive to the term  $D_X^2 \ (\delta_{CGRL} - \delta_{DQ})^2$  than it is to the term  $\delta_{CGRL}^2$ . Under these circumstances it is impossible to find the true null, where  $\delta_{CGRL} = \delta_{DQ} = 0$ , among a neighboring infinity of apparent nulls that are due to the vanishing of  $(\delta_{CGRL} - \delta_{DQ})^2$ .

High frequency decreases accuracy because of stray circuit capacitances and because of inductance in the bridge rheostats. Inspection of the Type 1650 Bridge or its schematic diagram will reveal several devices that have been added to the circuit to compensate for some of these unavoidable reactances.

When the unknown impedance is very large, the bridge's residual capacitance and the capacitance of the leads must be taken into account. Similarly, when the unknown impedance is very small, one must correct for residual bridge and lead inductance.

**Exercise:** Add a residual inductance  $L_0$  in series with the unknown in the series-capacitance bridge. Write down the new balance equation and derive the formula for the actual dissipation factor of the unknown,  $D_{\rm X} = \omega R_{\rm SX} C_{\rm SX}$ , in terms of the measured dissipation factor  $D_{\rm M} = \omega \vec{R}_{\rm DQ} C_{\rm ST}$ .

### SOME EXPERIMENTS

1. Remove the bridge from its case and inspect the parts. Note the 8 adjusting screws that are used to trim the *CGRL* rheostat's tracking with its dial. Locate the transformer, the standard capacitor, and the metal-film ratio-arm resistors.

2. Balance the bridge with a low-loss capacitor ( $D \le 0.005$ ) connected to the "unknown" terminals. Notice how little the DQ dial affects the balance. How small a deviation from the balance setting of the CGRL dial can you observe? Deliberately introduce an error of about 100 per cent in the DQ dial and readjust the CGRL dial for a minimum meter reading. How much has the CGRL dial reading changed? Can you still detect a 1 per cent error?

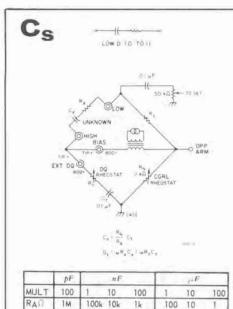
3. Connect a low-loss capacitor and a resistor in parallel to make a D of 2 or 3. Observe the sliding null and the very slow convergence of the nulling procedure without Orthonull. Try again with Orthonull switched in.

4. Measure  $L_S$  or  $L_P$  and Q of an iron-core inductor, and explain why the measured results depend on the bridge oscillator level.

5. Measure the capacitance of a reverse-biased diode or transistor collector-to-base junction on the  $C_{\rm S}$  bridge as a function of bias voltage, which can be applied through the bridge's bias connection. Note from the bridge schematic that any diode leakage current must flow through the bridge ratio arm. On the 100-pF range,  $R_{\rm B}$  is 1 M $\Omega$ , so that 1  $\mu$ A of leakage current causes a 1-volt drop in series with the bias supply.

6. Connect a capacitor and resistor in series to obtain a D of about 0.2. Measure  $C_S$  and D on the  $C_S$  bridge and  $C_P$  and D on the  $C_P$  bridge. Do the two D readings agree? Are the measured  $C_S$  and  $C_P$  in agreement with (18) and (19)? What should D be at 10 kHz? Using an external oscillator to drive the bridge, measure D at 10 kHz.





a. Turn GENERATOR switch to BAT CHECK position. If the meter pointer is not in the BAT sector, replace the batteries.

b. Turn GENERATOR switch to AC EXTERNAL or AC INTERNAL 1 kHz.

c. Turn PARAMETER switch to  $C_s$ . d. Connect the unknown so that most stray capacitance is between the LOW terminal and the 1650-B case.

e. Turn ORTHONULL  $^{\textcircled{R}}$  switch to OUT.

f. Turn OSC LEVEL clockwise. The panel control affects only the internal oscillator.

g. Turn DQ dial near 0.05 on the LOW D scale.

h. Turn CGRL dial near 11.

i. Adjust DET SENS for about 6 divisions deflection.

j. Turn MULTIPLIER switch for minimum meter reading.

k. Alternately adjust, first the CGRL dial, then the DQ dial for the best null, increasing the DET SENS as needed.

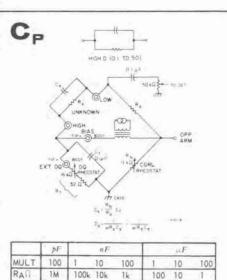
I. ORTHONULL<sup>®</sup> is not used on this bridge unless the DQ dial reading times f(kHz) approaches or exceeds 1.

m. If the DQ dial goes into the uncalibrated portion, the unknown should be measured as Cp.

n. The series capacitance of the unknown equals the product of the CGRL-dial reading and the MUL-TIPLIER-switch setting.

• The D equals the reading on the DQ dial times f (kHz).

p. Turn GENERATOR switch to OFF.



a. Turn GENERATOR switch to BAT CHECK position. If the meter pointer is not in the BAT sector,

replace the batteries.

b. Turn GENERATOR switch to AC EXTERNAL or AC INTERNAL 1 kHz.

c. Turn PARAMETER switch to  $C_p$ . Large electrolytics should be measured at a low frequency (120 Hz) for greater accuracy.

d. Connect the unknown so that most stray capacitance is between the LOW terminal and the 1650-B case.

e. Turn ORTHONULL $^{(\!R\!)}$  switch to OUT.

f. Turn OSC LEVEL clockwise. The panel control affects only the internal oscillator.

g. Turn DQ dial near 0.2 on the HIGH D scale.

h. Turn CGRL dial near 11.

i. Adjust DET SENS for about 6 divisions deflection.

j. Turn MULTIPLIER switch for minimum meter reading.

k. Alternately adjust, first the DQ dial, then the CGRL dial for the best null, increasing the DET SENS as needed.

1. ORTHONULL<sup>®</sup> switch should be set to IN if the DQ dial reading times 1/f (kHz) approaches or exceeds 1.

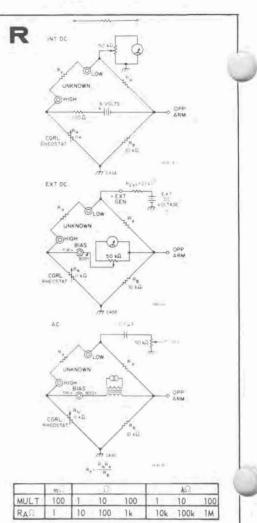
m. If the DQ dial reaches the stop at 0.1, the unknown should be measured as  $C_s$ .

n. The parallel capacitance of the unknown equals the product of the CGRL-dial reading and the MUL-TIPLIER-switch setting.

 The D equals the reading on the DQ dial times 1/f (kHz).

p. Turn GENERATOR switch OFF.

# Туре 1650-В



a. Check mechanical zero of meter. b. Turn GENERATOR switch to the BAT CHECK position. If the meter pointer is not in the BAT sector, replace the batteries.

c. Turn GENERATOR switch to the desired generator source. The OSC LEVEL control affects only the internal oscillator.

d. Turn ORTHONULL<sup>®</sup> switch to OUT and PARAMETER switch to R. e. Turn CGRL dial near 11.

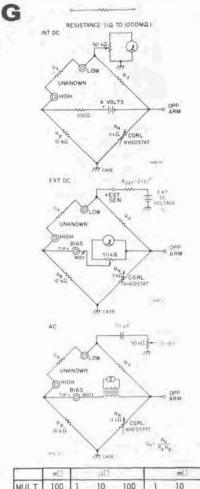
f. Adjust DET SENS control for about 6 divisions deflection.

g. Turn MULTIPLIER switch for minimum reading to the left of center if making a dc measurement. Null as usual if making an ac measurement. (DQ rheostat not in the circuit.)

h. Adjust CGRL dial for best ac null, or zero the pointer if using dc. If ac null is not sharp, a reactive balance may be necessary, see instruction manual.

i. The unknown resistance is the CGRL-dial reading multiplied by the MULTIPLIER switch setting. j. Turn GENERATOR switch to OFF.

# OPERATING INSTRUCTIONS



MULT	100	1	10	100	1	10	100
RAD	1M	100k	10k	1k	100	10	1

a. Check mechanical zero of meter. b. Turn GENERATOR switch to the BAT CHECK position. If the meter pointer is not in the BAT sector, replace the batteries.

c. Turn GENERATOR switch to the desired generator source. The OSC LEVEL control affects only the internal oscillator.

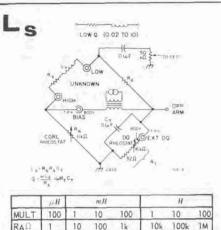
d. Turn ORTHONULL  $^{(\!R\!)}$  switch to OUT and PARAMETER switch to G.

e. Turn CGRL dial near 11.

f. Adjust DET SENS control for about 6 divisions deflection.

g. Turn MULTIPLIER switch for minimum reading to the left of center if making a dc measurement. Null as usual if making an ac measurement. (DQ rheostat not in the circuit.) h. Adjust CGRL dial for best ac null, or zero the pointer if using dc, lf ac null is not sharp, a reactive balance may be necessary, see instruction manual.

i. The unknown conductance is the CGRL-dial reading multiplied by the MULTIPLIER switch setting. j. Turn GENERATOR switch to OFF.



a. Turn GENERATOR switch to BAT CHECK. If the meter pointer isn't in the BAT sector, replace the batteries.

b. Turn GENERATOR switch to AC EXTERNAL or AC INTERNAL 1 kHz. Air core rf chokes should be measured at a high frequency (10 kHz) to get a reasonable Q.

c. Turn PARAMETER switch to Ls. d. Connect unknown so that most stray capacitance is between the LOW terminal and the 1650-B case. e. Turn ORTHONULL® switch to OUT.

f. Turn OSC LEVEL clockwise. The panel control affects only the internal oscillator. Use full output except for nonlinear unknowns. Iron core inductors are often nonlinear.

g. Turn DQ dial near 4 on the LOW Q scale.

h. Turn CGRL dial near 11.

i. Adjust DET SENS for about 6 divisions deflection.

i. Turn MULTIPLIER switch for minimum meter reading.

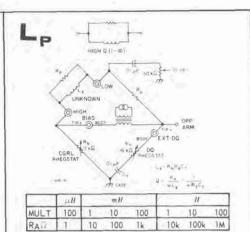
k. Alternately adjust the CGRL and DQ dials for the best null, DQ dial first, increasing the DET SENS as needed. Null means bring the pointer as near to the center of the meter as possible. Usually it won't be possible to center the pointer. I. ORTHONULL<sup>®</sup> should be switched IN if the DQ-dial reading times f(kHz) approaches or is less than 1. m. If a sharp null cannot be obtained and the Q dial is near 10,

switch to Lp.

n. The series inductance of the unknown equals the product of the CGRL-dial reading and the MULTI-PLIER-switch setting.

o. The Q of the unknown equals the Q-dial reading times f (kHz).

p. Turn GENERATOR switch OFF.



a. Turn GENERATOR switch to BAT CHECK. If the meter pointer isn't in the BAT sector, replace the batteries.

b. Turn GENERATOR switch to AC EXTERNAL or AC INTERNAL 1 kHz.

c. Turn PARAMETER switch to Lp. d. Connect unknown so that most stray capacitance is between the LOW terminal and the 1650-B case. e. Turn ORTHONULL<sup>®</sup> switch to OUT.

f. Turn OSC LEVEL clockwise. The panel control affects only the internal oscillator. Use full output except for nonlinear unknowns. Iron core inductors are often nonlinear.

g. Turn DQ dial near 5 on the HIGH Q scale.

h. Turn CGRL dial near 11.

i. Adjust DET SENS for about 6 divisions deflection.

j. Turn MULTIPLIER switch for minimum meter reading.

k. Alternately adjust the CGRL and DQ dials for the best null, CGRL first, increasing the DET SENS as needed. Null means bring the pointer as near to the center of the meter as possible. Usually it won't be possible to center the pointer.

1. ORTHONULL<sup>®</sup> is not used on this bridge unless the DQ dial reading times 1/f (kHz) approaches 1 or less.

m. If a sharp null cannot be obtained, the unknown is too lossy and must be measured as  $L_s$ , or the unknown is not inductive.

n. The parallel inductance of the unknown equals the product of the CGRL-dial reading and the MULTI-PLIER-switch setting.

o. The Q of the unknown equals the dial reading times 1/f (kHz).

p. Turn GENERATOR switch to OFF.

## Type1650-B IMPEDANCE BRIDGE

- measures L, C, and loss; R and G
- 1% accuracy

The 1650 Impedance Bridge will measure the inductance and storage factor, Q, of inductors\*, the capacitance and dissipation factor, D, of capacitors, and the ac and dc resistance or conductance of resistors.

In the laboratory it is extremely useful for measuring the circuit constants in experimental equipment, testing



### specifications

RANGES OF MEASUREMENT

20 Hz to 20 kHz, internal 1 kHz and dc

portable, self-contained, battery-operated

preliminary samples, and identifying unlabeled parts. In the shop and on the test bench it has many applications for testing and component sorting.

Three-terminal measurements can be made in the presence of considerable stray capacitance to ground.

### DESCRIPTION

This bridge is completely self-contained and portable. Battery-powered, low-drain transistor oscillator and detector are included. The panel meter indicates both dc and ac bridge unbalances.

The measured quantities, R, G, L, C, D, and Q, are indicated directly on dials with constant-percentage-accuracy logarithmic scales. Multiplier and the units of measurement are indicated by the range setting.

The bridge circuit elements are high-quality, stable components that ensure long-term accuracy. The Orthonull® balance finder, a patented mechanical-ganging device, is used to make a low-Q (high-D) balance possible without a sliding null. This mechanism, which may be switched in or out as desired, adds accuracy as well as convenience to low-Q measurements that are practically impossible on other impedance bridges.

The Flip-Tilt case provides a handle and a captive, protective cover that allows the bridge panel to be tilted and held firmly at any angle.

\* Including such low-Q inductors as rf coils measured at 1 kHz.

ACCURACY

	20 Hz to 20 kHz†	DC	Residuals	
Capacitance 1 pF to 1100 μF, series or parallel, 7 ranges	±1% ±1 pF		≈0.5 pF	
Inductance 1 μH to 1100 H, series or parallel, 7 ranges	±1% ±1 μH		≈ 0.2 µH	
Resistance ac or dc, 1 m $\Omega$ to 1.1 M $\Omega$ , 7 ranges	$\pm 1\% \pm 1$ mΩ	$\pm1\%,1~\Omega$ to 100 kΩ, ext supply or detector required $>100~$ kΩ and $<1~\Omega.$	≈1 mΩ	
Conductance ac or dc, 1 nanomho to 1.1 mhos, 7 ranges	$\pm1\%$ $\pm1$ nanomho	$\pm$ 1%, 10 micromhos to 1 mho, ext supply or detector required <10 micromhos.		
Dissipation Factor, D, at 1 kHz, 0.001 to 1 of series C, 0.1 to 50 of parallel C.	±5% ±0.001 at 1 kHz and lower			
Storage Factor, Q, at 1 kHz, 0.02 to 10 of series L, 1 to 1000 of parallel L.	$\frac{1}{Q} \text{ accurate to} \\ \pm 5\% \pm 0.001 \text{ at} \\ 1 \text{ kHz or lower}$			
			-	

+ Bridge operates up to 100 kHz with reduced accuracy.

#### GENERAL

Generator: Internal; 1 kHz  $\pm 2\%$ . Type 1310 or 1311 Oscillator recommended if external generator is required. Internal dc supply, 6 V, 60 mA, max.

Detector: Internal or external; internal detector response flat or selective at 1 kHz; sensitivity control provided. Type 1232-A Tuned Amplifier and Null Detector is recommended if external detector is required. Combination of 1311 oscillator and 1232 detector is available as the Type 1240 Bridge Oscillator-Detector.

**DC Polarization:** Capacitors can be biased to 600 V from external dc power supply for series capacitance measurements.

Power Required: 4 size-D cells, supplied.

Accessories Required: None. Earphones can be used for high precision at extremes of bridge ranges.

BOSTON • PHILADELPHIA • CHICAGO • ORLANDO • DALLAS NEW YORK • SYRACUSE • LOS ANGELES • SAN FRANCISCO WASHINGTON, D.C. • CLEVELAND • TORONTO • MONTREAL Accessories Available: Type 1650-P1 Test Jig.

Mounting: Flip-Tilt Cabinet.

Dimensions (width x height x depth): Portable, 13 x  $63\!\!\!/4$  x  $12!\!\!/4$  in. (330 x 175 x 315 mm); rack, 19 x  $12!\!\!/4$  x  $4!\!\!/_8$  in. (485 x 315 x 105 mm).

Net Weight (est): Portable, 17 lb (8 kg); rack, 18 lb (8.5 kg).

snipping weight	est): Portable, 21 ib (10 kg);	rack, 30 ID (13.5 kg).	
Detalan			

Number	Description	
	1650-B Impedance Bridge	
1650-9702	Portable Model	
1650-9703	Rack Model	

## GENERAL RADIO COMPANY

WEST CONCORD, MASSACHUSETTS 01781